

PRIOR ART

FIGURE 1

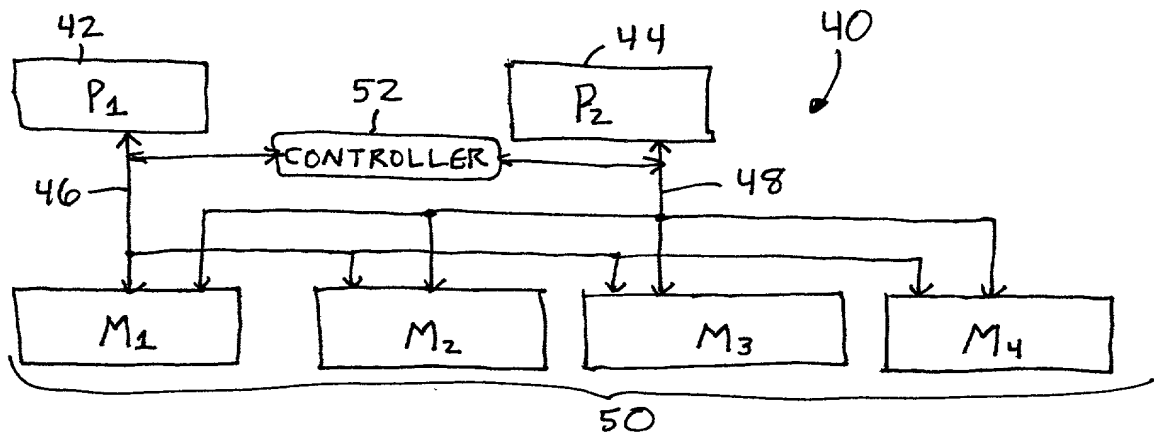


FIGURE 2

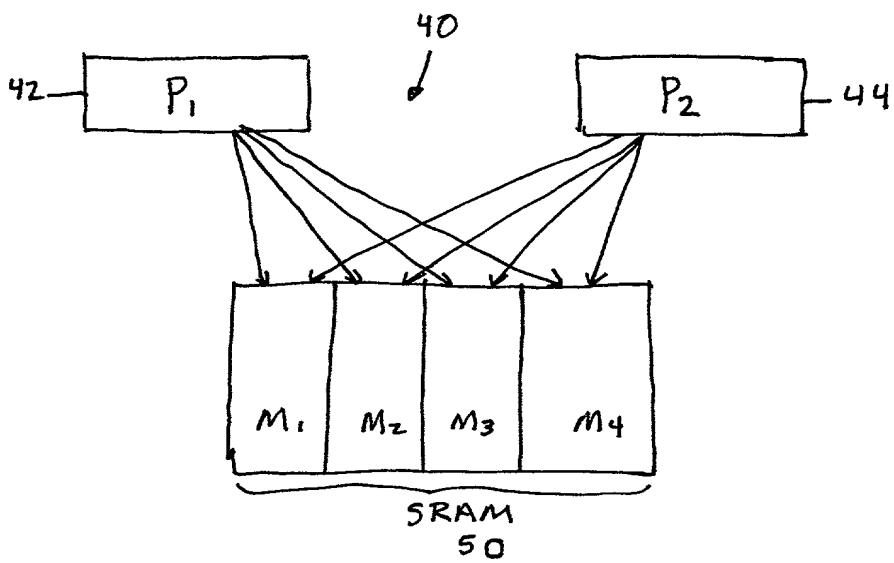


FIGURE 3

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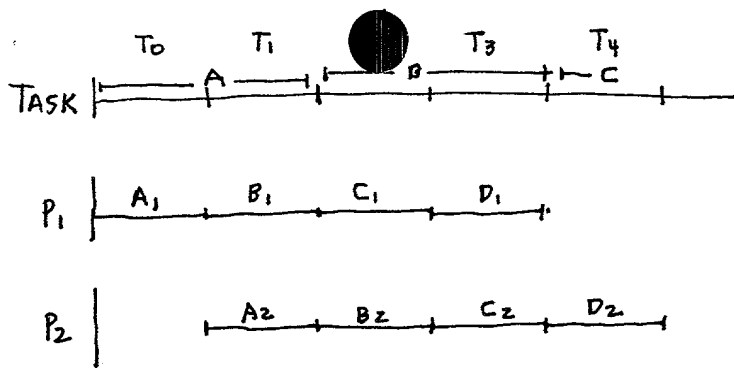


FIGURE 4

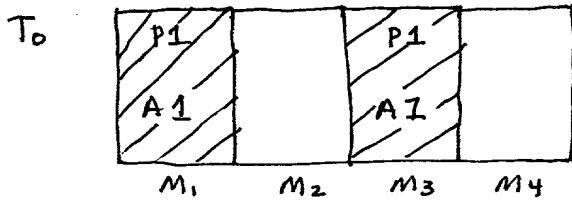


Figure 5A

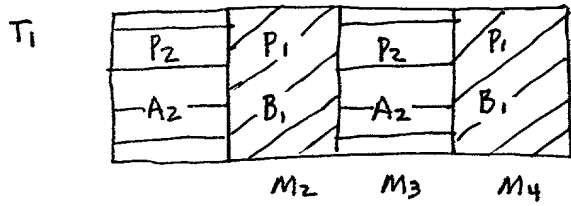


Figure 5B

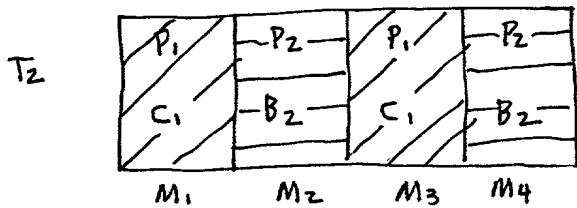


Figure 5C

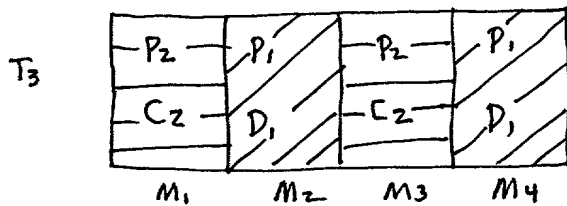


Figure 5D

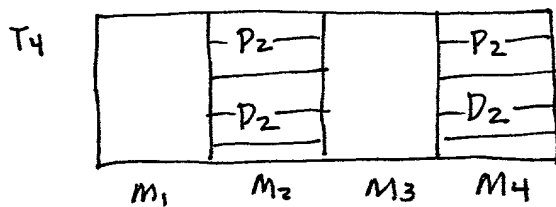
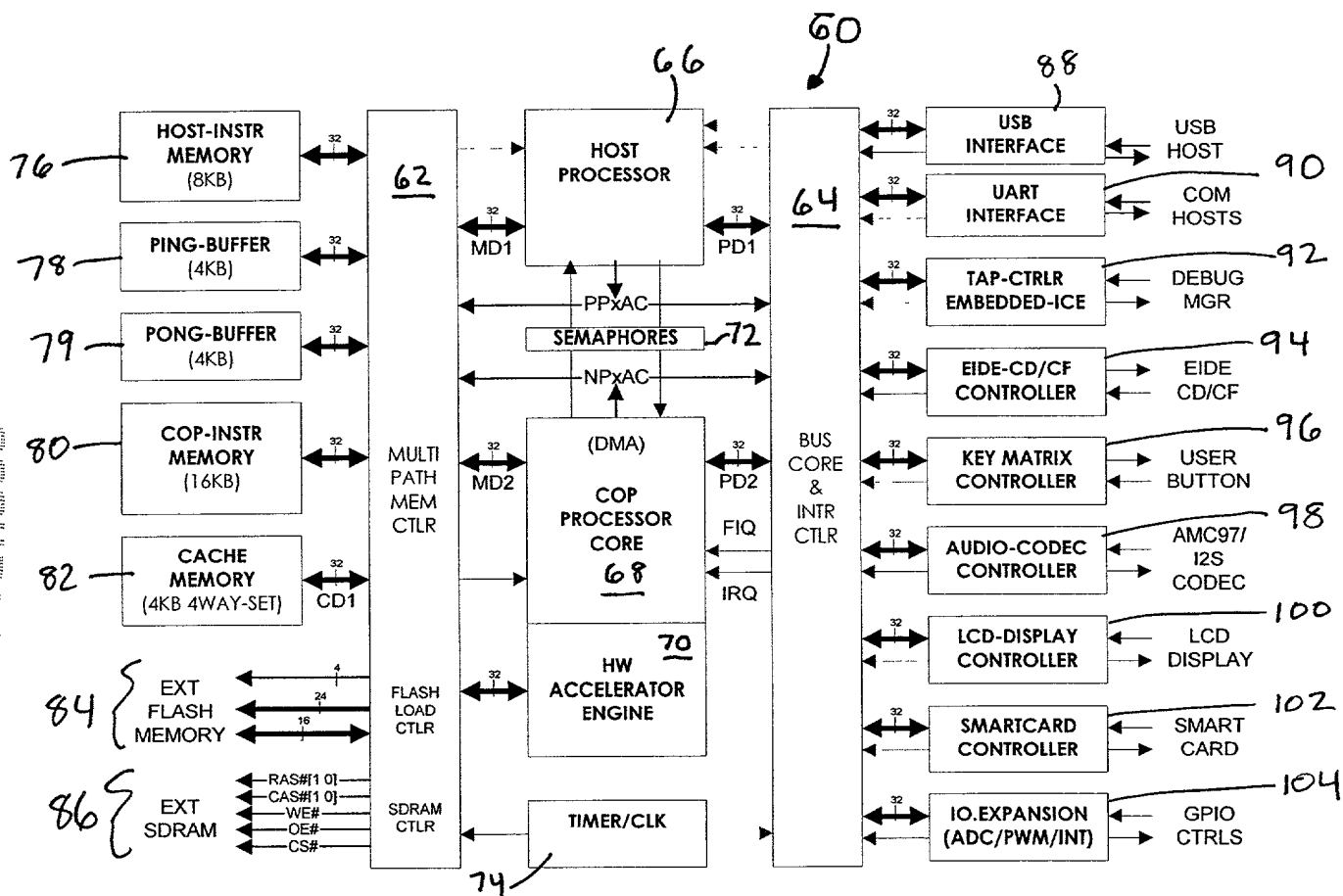


Figure 5E

FIG. 6



Functional Block Diagram of SDMI-SOC

Figure 6

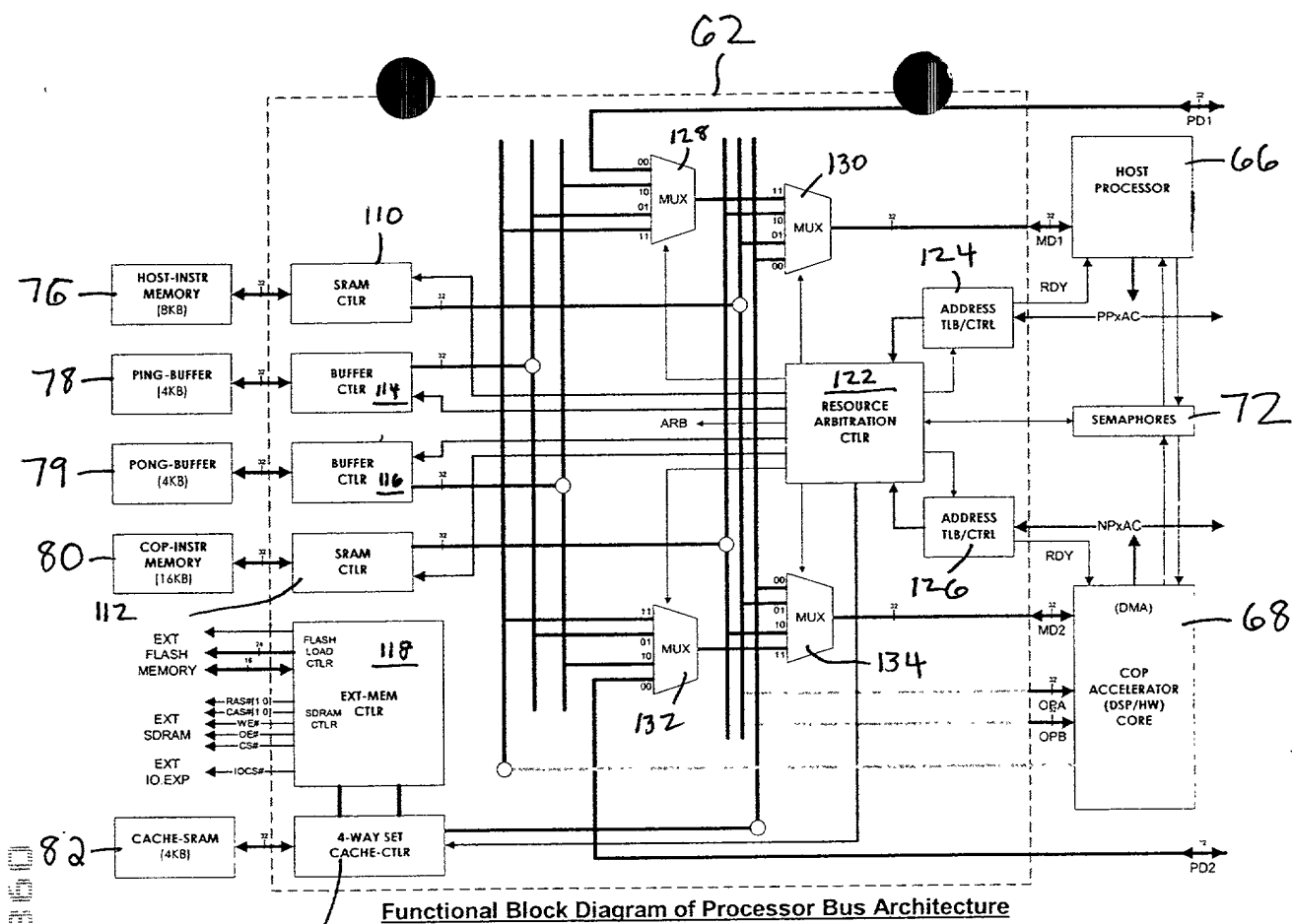


Figure 7

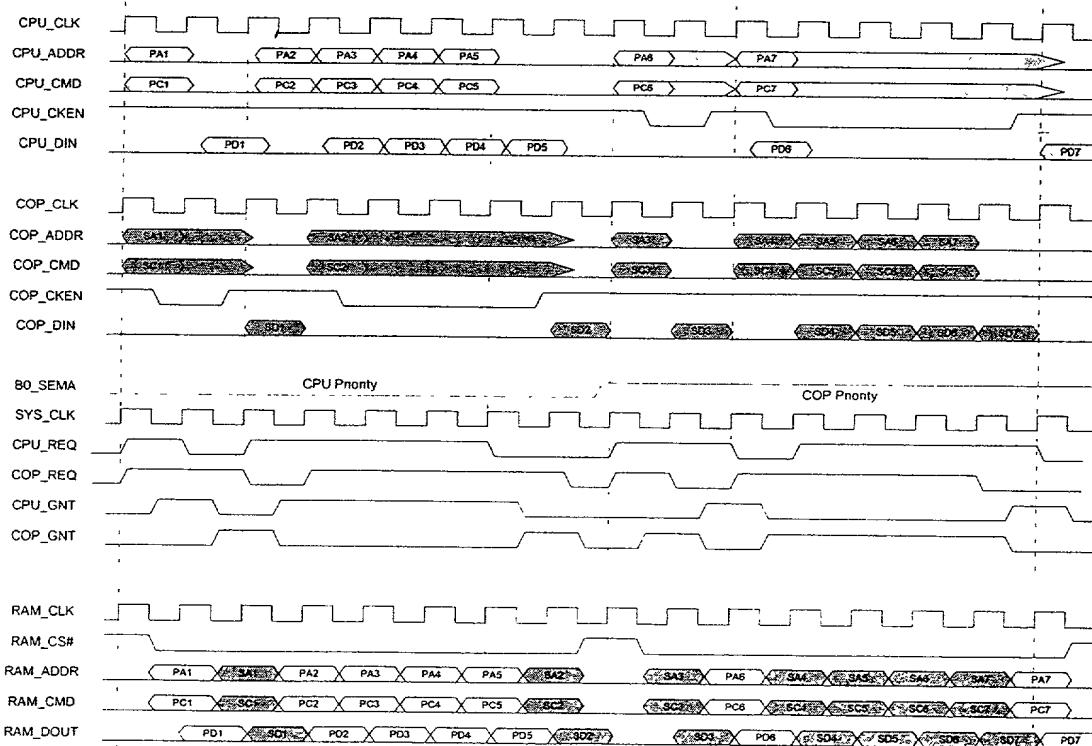
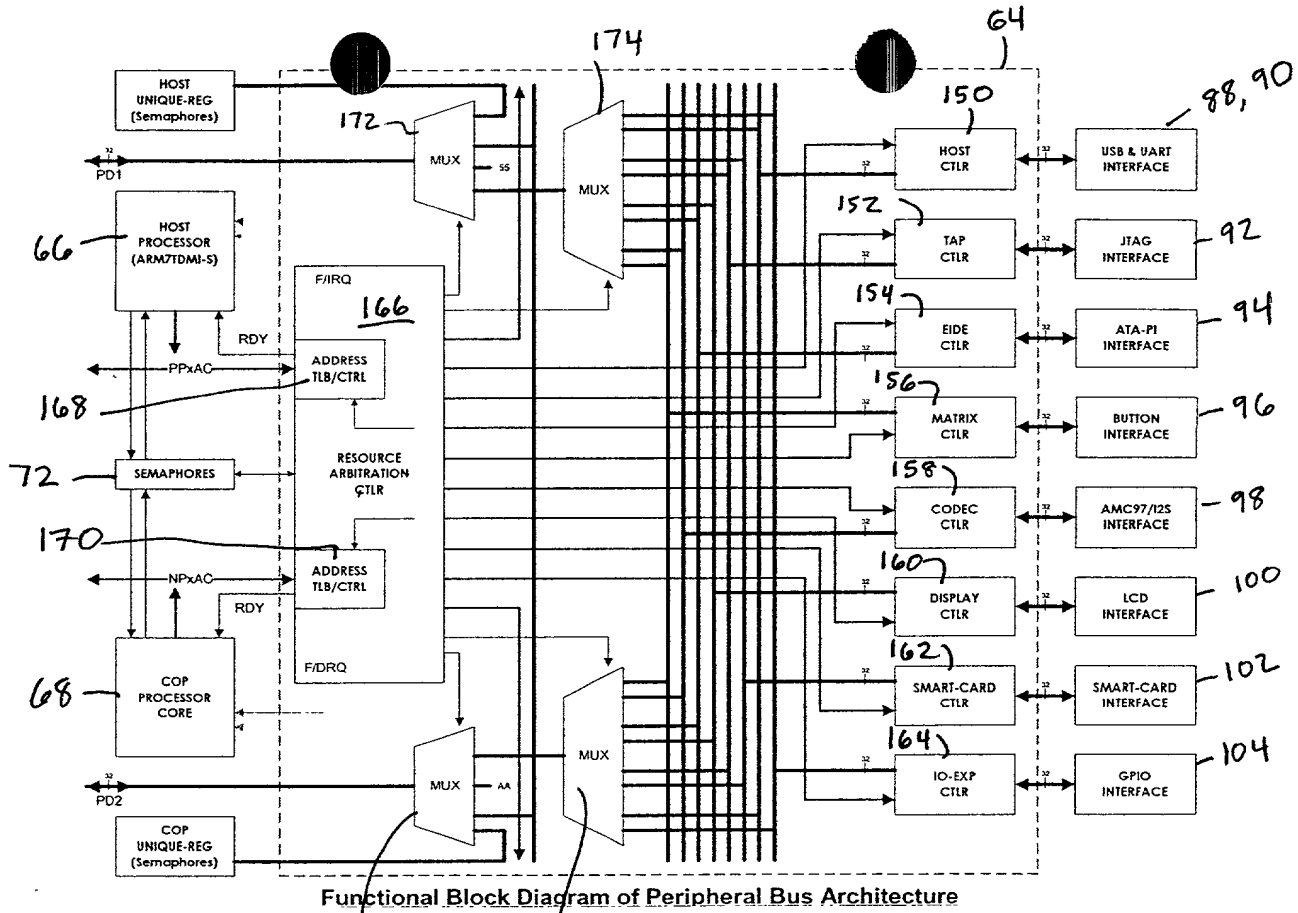


Figure 8



Functional Block Diagram of Peripheral Bus Architecture

Figure 9

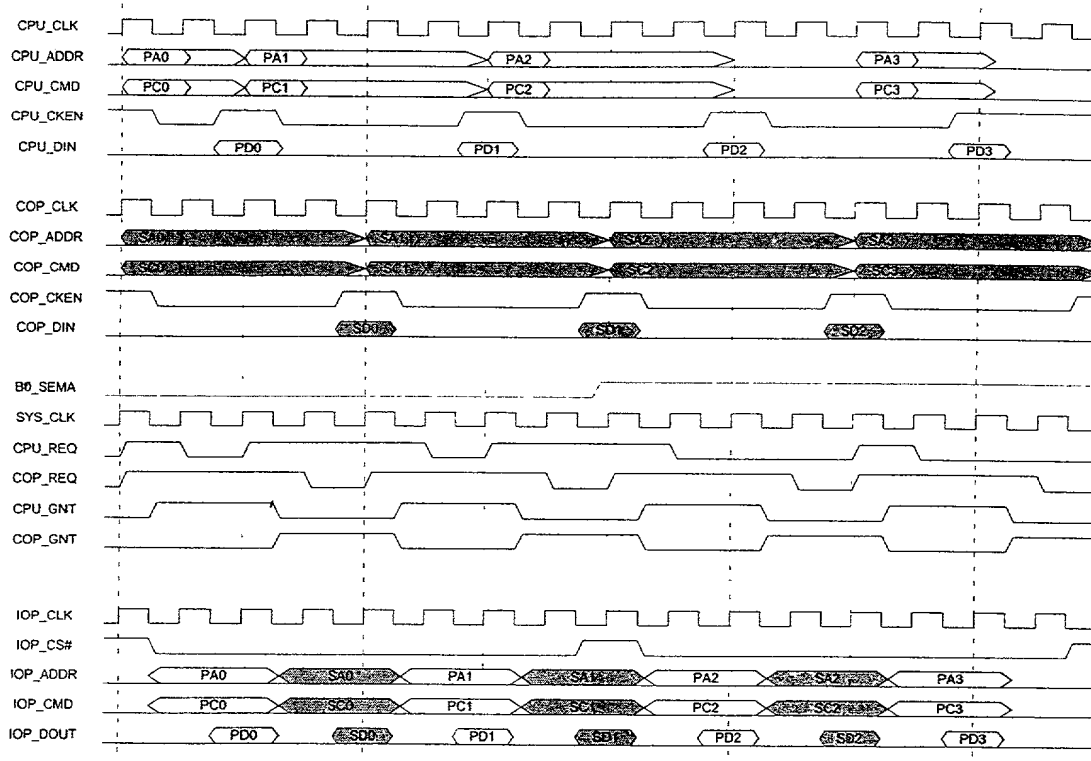


Figure 10